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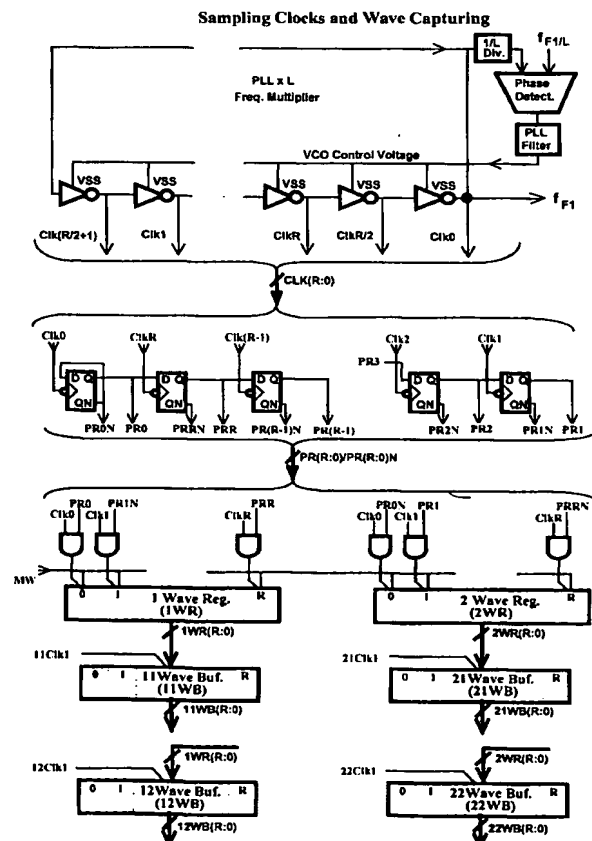
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(54) Title: DIGITAL SIGNAL PROCESSING OF MULTI-SAMPLED PHASE



(57) Abstract: The DSP MSP invention provides an implementation of programmable algorithms for analyzing a very wide range of low and high frequency wave-forms. The DSP MSP comprises a synchronous sequential processor (SSP) for real time capturing and processing of in-coming wave-form and a programmable computing unit (PCU) for controlling SSP operations and supporting adaptive signal analysis algorithms. The DSP MSP further comprises a circuit for Sequential Data Recovery from Multi Sampled Phase (SDR MSP), for a receiver of an optical waveform.

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B. FIELDS SEARCHED

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Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Y	column 1, line 15 -column 9, line 21; claims 1-4; figures 2-8	3,7-12, 15,25, 41,44,67

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Y	the whole document	7-9,11, 12,15, 21,25, 32,33, 39-41, 44,49, 51,66,67

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☒ Patent family members are listed in annex.

*& document member of the same patent family

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INTERNATIONAL SEARCH REPORT

International Application No

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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